PEVC

Our Reference: 2024.30

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Lowrey, et al

Application Number: 10/072,369 Filing Date: February 8, 2002

Art Unit/Examiner: 2814/Hoai V. Pham

Title: PROGRAMMABLE RESISTANCE MEMORY ELEMENT AND METHOD

FOR MAKING SAME

CERTIFICATE OF MAILING BY EXPRESS MAIL UNDER 37 CFR 1.10

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 Sir:

Enclosed in this correspondence is:

- 1. Response to Election/Restriction Requirement of February 18, 2003.
- 2. Request for Three-Month Extension of Time
- 3. Information Disclosure Statement under 37 CFR 1.97(b)(3)
- 4. Form PTO/SB/08A
- 5. Copy of U.S. Patent No. 6,031,287

I hereby certify that this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 in an envelope addressed to:

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